



1 Pu

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q80572

Mitsuhiro SHIMAMOTO

Appln. No.: 10/803,983

Group Art Unit: 2816

Confirmation No.: 6913

Examiner: Minh T. NGUYEN

Filed: March 19, 2004

For: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE**

RESPONSE UNDER EX PARTE QUAYLE PRACTICE

MAIL STOP AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated May 3, 2005, please amend the above-identified application as follows on the accompanying pages.

TABLE OF CONTENTS

| | |
|--------------------------------------|---|
| AMENDMENTS TO THE SPECIFICATION..... | 2 |
| AMENDMENTS TO THE CLAIMS | 3 |
| REMARKS | 5 |